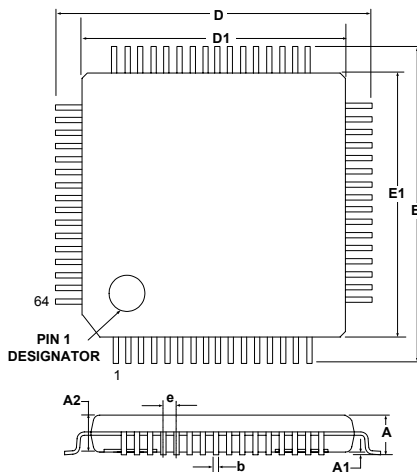


SELECTED ELECTRICAL SPECIFICATIONS TA = -40°C to +85°C unless otherwise specified.

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
GLOBAL CHARACTERISTICS					
Supply Voltage		2.7		3.6	V
Supply Current (CPU active)	Clock=100MHz		50		mA
	Clock=1MHz		0.6		mA
	Clock=32kHz		16		μA
Supply Current (shutdown)	Oscillator off; VDD Monitor Enabled		10		μA
	Oscillator off; VDD Monitor Disabled		0.4		μA
Clock Frequency Range		DC		100	MHz
INTERNAL CLOCKS					
Oscillator Frequency		24.0	24.5	25.0	MHz
PLL Frequency		96	98	100	MHz
A/D CONVERTER					
Resolution			10		bits
Integral Nonlinearity				± 1	LSB
Differential Nonlinearity	Guaranteed Monotonic			± 1	LSB
Signal-to-Noise Plus Distortion		59			dB
Throughput Rate				100	ksp/s
D/A CONVERTERS					
Resolution			12		bits
Differential Nonlinearity	Guaranteed Monotonic			± 1	LSB
Output Settling Time			10		μS

PACKAGE INFORMATION



	MIN (mm)	NOM (mm)	MAX (mm)
A	-	-	1.20
A1	0.05	-	0.15
A2	0.95	-	1.05
b	0.17	0.22	0.27
D	-	12.00	-
D1	-	10.00	-
e	-	0.50	-
E	-	12.00	-
E1	-	10.00	-

C8051F120DK DEVELOPMENT KIT

