

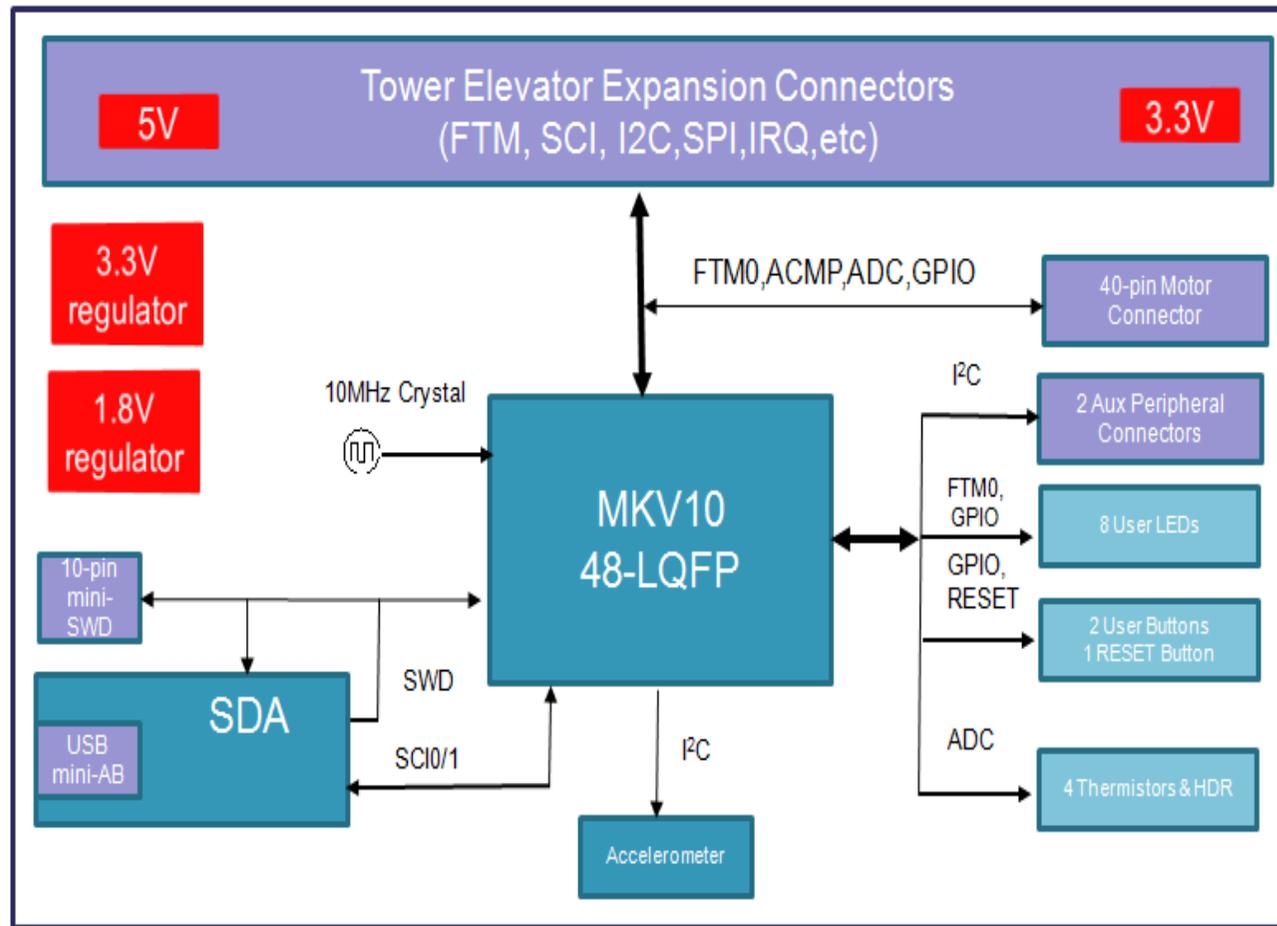
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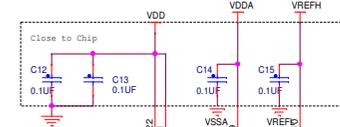
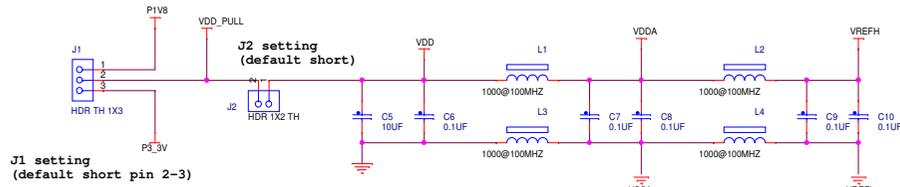
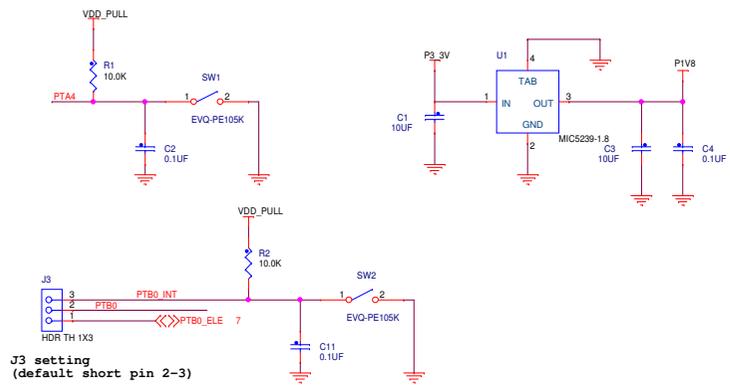
**Revisions**

Rev	Description	Date	Approved
X1	Initial Release	26-Dec-12	
A	Release to A085 (Production)	6-Feb-13	
A1	DNP R65, populate R64	5-Mar-13	
B	1. Divide the K20's voltage from the KV10, using the K20's internal regulator to supply the K20 2. Change the LED current limit resistors to 1Kohm. 3. Delete the LED on PFB0	11-Jul-13	
B1	Change the elevator connector's pinA63's defination	14-Aug-13	
B2	Updated the KV10's pin definition and the relative net-names. Due to the connection is not changed, so the PCB is not changed and updated: 1.Change the net-name ADC0_DMG/PTE17 to ADC0_DM1/PTE17 2.Change the net-name ADC1_SE1/PTE24 to PTE24 3.Change the net-name ADC0_SE0/PTE16/ADC1_SE0 to ADC1_SE1/PTE16/ADC1_SE0 Also, updated the net-names for the readability: 1.Change the net-name SDA0/PTC7_ELE to SDA0/PTC7_OTHER 2.Change the net-name SCL0/PTC6_ELE to SCL0/PTC6_OTHER 3.Change the net-name PFB3/ADC1_SE2_AUX to PFB3/ADC1_SE2_OTHER 4.Change the net-name ELEV_RXD0 to RXD0_ELEV 5.Change the net-name ELEV_TXD0 to TXD0_ELEV 6.Change the net-name ELEV_RXD1 to RXD1_ELEV 7.Change the net-name ELEV_TXD1 to TXD1_ELEV	7-Nov-13	
B3	1. Updated the J14's shunt default setting 2. Updated the block diagram	12-Dec-13	

			
ICAP Classification: FCP: _____ FLUQ: X PUB: _____			
Drawing Title: <b>TWR-KV10Z32</b>			
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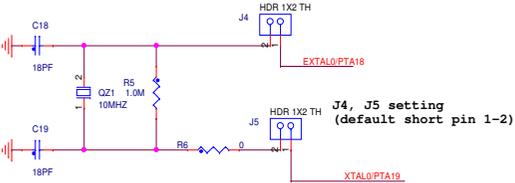
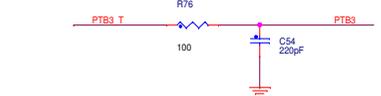
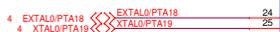
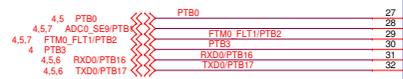
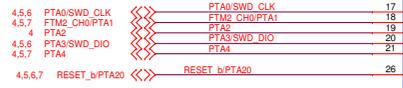


# MKV10Z32VLF7 Controller

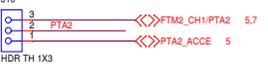


J3 setting (default short pin 2-3)

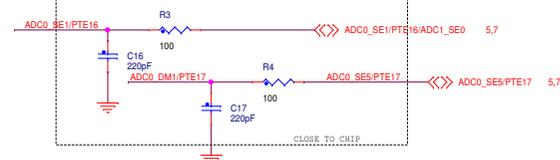
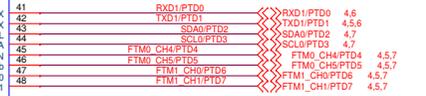
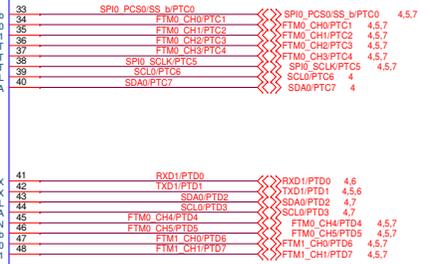
J1 setting (default short pin 2-3)



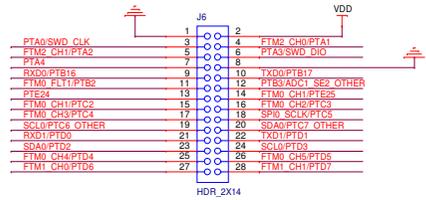
J4, J5 setting (default short pin 1-2)



J7- J10 setting (default short pin 1-2)



## AUX Connector1



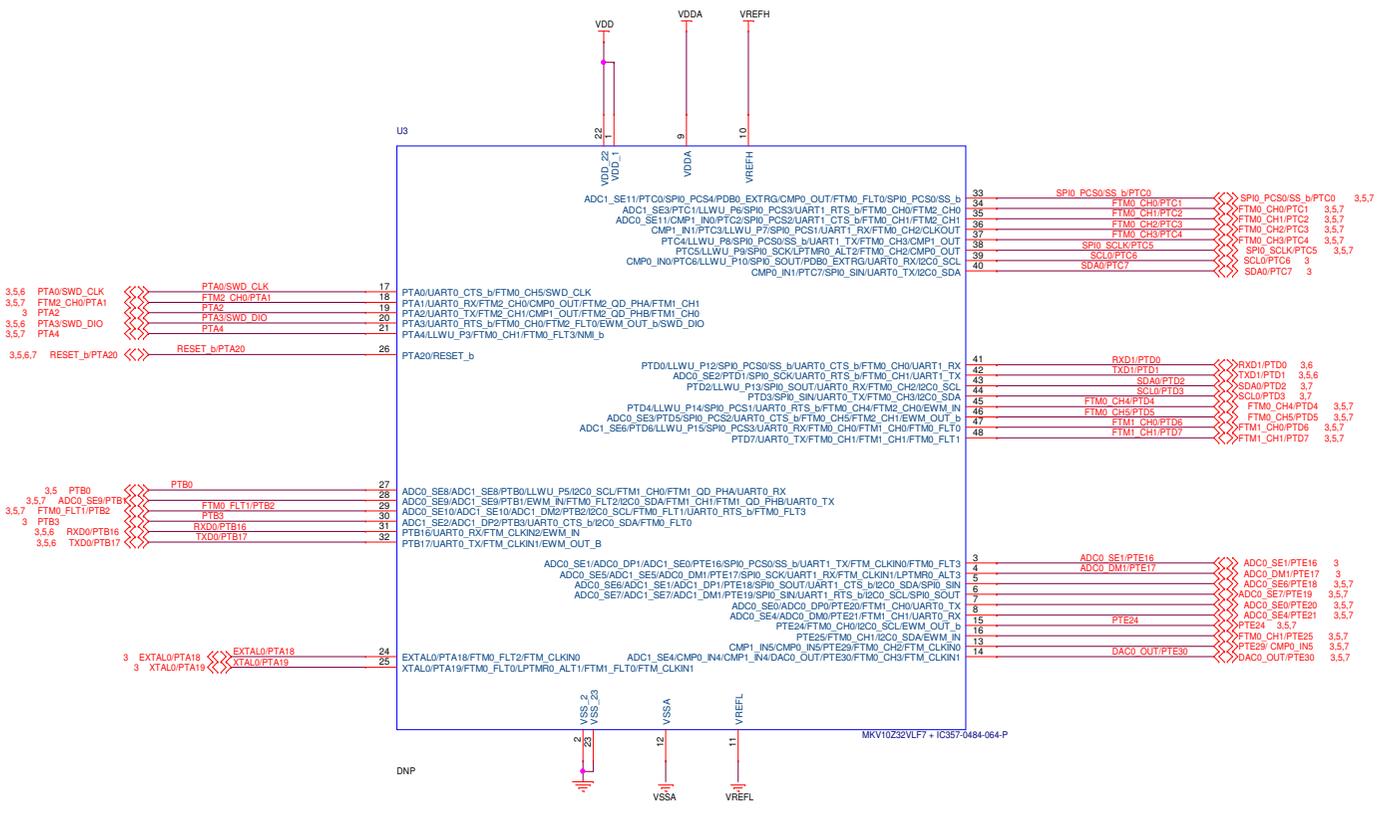
ICAP Classification: FCP, FLUQ, X, PUBI

Drawing Title: **TWR-KV10Z32**

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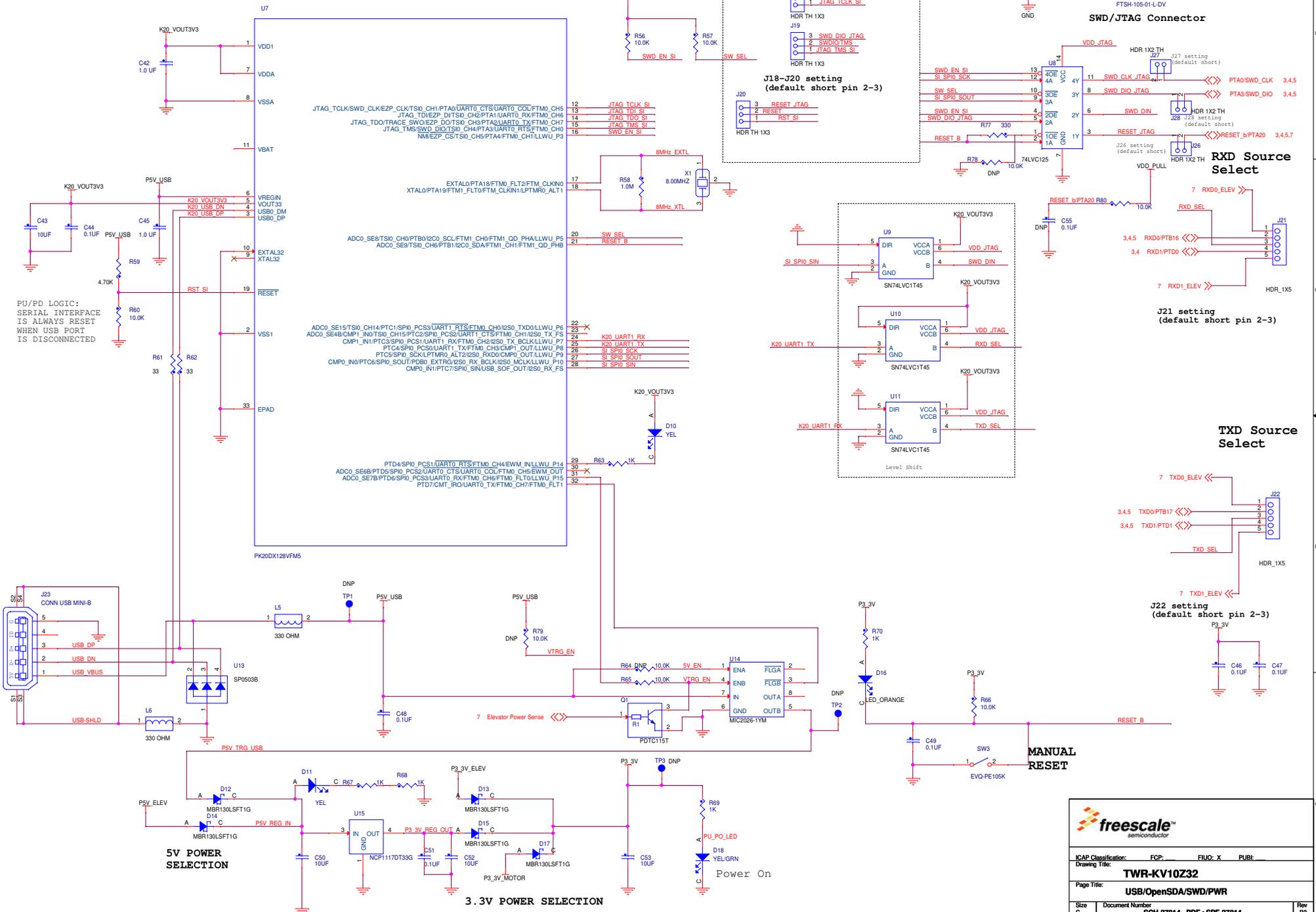
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