Factory Programming Order & VERIFICATION SHEET

MC95FG308 ☐ - NOF ☐☐

↑ Package Information : D, U, L, M

Please fill in the bold areas.

1. Customer Information

Company Name	
Application	
Order Date	YYYY MM DD
Tel : Fax : Name & Signature :	

2. Device Information

Package	☐ 32 SOP (D) ☐ 32 QFN (U) ☐ 32 LQFP (L) ☐ 28 SOP (M) ☐ Wafer Chip sales code:( )
ROM Code Size	8K byte

4. Writing Option

- ☐ Code Read Protection
- ☐ Xdata Read Protection
- ☐ Disable RESETB pin
- ☐ Enable Xin, Xout
- ☐ Enable Sub-Xin, Xout

Boot code area

- ○ 256B (0x100~0x1FF)
- ○ 768B (0x100~0x3FF)
- ○ 1.7kB (0x100~0x7FF)
- ○ 3.8kB (0x100~0xFFF)
- ☐ Enable HARD lock

(Please check mark info ☐)

3. code file Information

Check Sum	
File Name	.HEX

You must fill unused area with 0x00

(Code area : 0000_H ~ $1FFF_H$)

5. CODE Verification

Verification Date :
Check Sum :
Option :
Name:

6. Remark

Marking : standard

ABOV SEMICONDUCTOR

Factory Programming Order & VERIFICATION SHEET

MC95FG208 ☐ - NOF ☐☐

↑ Package Information : D, R, M, H

Please fill in the bold areas.

1. Customer Information

Company Name	
Application	
Order Date	YYYY MM DD
Tel : Fax : Name & Signature :	

2. Device Information

Package	☐ 20 SOP (D) ☐ 20 TSSOP (R) ☐ 16 SOP (M) ☐ 16 TSSOP (H) ☐ Wafer Chip sales code:( )
ROM Code Size	8K byte

4. Writing Option

- ☐ Code Read Protection
- ☐ Xdata Read Protection
- ☐ Disable RESETB pin
- ☐ Enable Xin, Xout
- ☐ Enable Sub-Xin, Xout

Boot code area

- ○ 256B (0x100~0x1FF)
- ○ 768B (0x100~0x3FF)
- ○ 1.7kB (0x100~0x7FF)
- ○ 3.8kB (0x100~0xFFF)
- ☐ Enable HARD lock

(Please check mark info ☐)

3. code file Information

Check Sum	
File Name	.HEX

You must fill unused area with 0x00

(Code area : 0000_H ~ $1FFF_H$)

5. CODE Verification

Verification Date :
Check Sum :
Option :
Name:

6. Remark

Marking : standard

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