INTEGRATED CIRCUITS

ERRATA SHEET

Date: 2009 July 23
Document Release: Version 1.0
Device Affected: LPC1343

This errata sheet describes both the functional problems and any deviations from the electrical specifications known at the release date of this document.

Each deviation is assigned a number and its history is tracked in a table at the end of the document.

2009 July 23



LPC1343 Erratasheet

Document revision history

| Rev | Date | Description |
|-----|--------------|---------------|
| 1.0 | July 23 2009 | First version |

LPC1343 Erratasheet

Identification

The typical LPC1343 devices have the following top-side marking:

LPC1343xxx

XXXXXXX

xxYYWW R[x]

The last/second to last letter in the third line (field 'R') will identify the device revision. This Errata Sheet covers the following revisions of the LPC1343:

| Revision Identifier (R) | Comment |
|-------------------------|-------------------------|
| 'A' | Initial device revision |

Field 'YY' states the year the device was manufactured. Field 'WW' states the week the device was manufactured during that year.

LPC1343 Erratasheet

Errata Overview - Functional Problems

| Functional Problem | Short Description | Device Revision the problem occurs in |
|-----------------------|-----------------------------------------------------------------------------------------------------------------------------------|---------------------------------------------|
| ISP.1 | When using In-System Programming (ISP) via the UART serial port, sector 0 cannot be erased if it is the only sector being erased. | , Y, |

Errata Overview - AC/DC Deviations

| AC/DC Deviation | Short Description | Device Revision the deviation occurs in |
|-----------------|-------------------|-----------------------------------------------|
| n/a | n/a | n/a |

Errata Notes

| Notes | Short Description | Device Revision the note applies to |
|-------|-------------------|-------------------------------------------|
| n/a | n/a | n/a |

Functional Problems of LPC1343

ISP.1: When using In-System Programming (ISP) via the UART serial port, sector 0 cannot be

erased if it is the only sector being erased.

Introduction: On the LPC13xx, programming, erasure and re-programming of the on-chip flash can be performed

using In-System Programming (ISP) via the UART serial port, and also, can be performed using In-Application Programming (IAP) calls directed by the end-user code. For In-System Programming (ISP) via the UART serial port, the ISP command handler (resides in the bootloader) allows erasure

of one or more sector (s) of the on-chip flash memory.

Problem: When using In-System Programming (ISP) via the UART serial port, sector 0 cannot be erased if it

is the only sector being erased. In-Application Programming (IAP) calls directed by the end-user

code are not affected by this problem.

Workaround: To erase sector 0, multiple sectors need to be selected.