

## Revision history

Use ABOV New S/W Version rule (2015. 9.21.Mon ~)

Version	Date	Description
V2.034.00	2018.12. 5.Wed	Add devices <ul style="list-style-type: none"><li>- AL4000</li><li>- A96L322</li><li>- A96G174</li><li>- A94Q116</li><li>- A94B428</li></ul> Fix <ul style="list-style-type: none"><li>- A96R150 was hidden device → open</li><li>- Global variable window<ul style="list-style-type: none"><li>→ Save/Load for each project</li></ul></li></ul>
V2.033.00	2018. 7. 2.Mon	Add devices <ul style="list-style-type: none"><li>- A96G140</li><li>- A96R140</li><li>- A96R150</li><li>- A96R725</li><li>- A96L302</li><li>- A94B428</li></ul> New feature <ul style="list-style-type: none"><li>- Symbolic debugging for IAR output (*.d51)</li></ul>
V2.032.00	2018. 3.26.Mon	Add devices <ul style="list-style-type: none"><li>- A96T356</li></ul> Fix <ul style="list-style-type: none"><li>- Infinite reset message with OCD1 dongle<ul style="list-style-type: none"><li>→ MC96F6432, MC96F8316</li></ul></li></ul>
V2.031.06	2018. 1.16.Tue	Fix <ul style="list-style-type: none"><li>- C procedure address range confliction</li></ul>
V2.031.05	2018. 1. 9.Tue	Fix <ul style="list-style-type: none"><li>- Improve connection between OCD1 dongle and target device (OCD1 vs Device initialization)</li></ul>
V2.031.04	2017.12.19.Tue	Fix

		<ul style="list-style-type: none"> <li>- GPIO : Reset default (ex: A96T218)</li> <li>- OCD initialization is not cleared during GPIO(reset) is Low</li> <li>- Add initialize sequence for these devices</li> </ul>
V2.031.01	2017.11.24.Fri	Add devices <ul style="list-style-type: none"> <li>- A94B316</li> </ul>
V2.031.00	2017.11.16.Thu	Add devices <ul style="list-style-type: none"> <li>- A96R739</li> </ul> Add feature <ul style="list-style-type: none"> <li>- Show CPU instruction bug in HEX download dialog box</li> <li>- M8051 : PUSH SP</li> <li>- CM8051 : PUSH SP, POP SP, DA A</li> </ul>
V2.030.00	2017. 9.15.Fri	Add <ul style="list-style-type: none"> <li>- Add A94B114 to KEIL plug-in</li> </ul> Modification <ul style="list-style-type: none"> <li>- A96R136 Lock / Unlock</li> </ul>
V2.029.00	2017. 8. 10.Thu	Fix <ul style="list-style-type: none"> <li>- A94B114 real time monitoring (XDATA)</li> <li>- A96T346 : bulk erase include pre-program</li> <li>- A97C450 : bulk erase exclude user configuration restoring</li> </ul> Modification <ul style="list-style-type: none"> <li>- Remove RUN Time display because following devices does not include it</li> <li>- A96T316/326/336/346</li> <li>- MC96F1206</li> </ul> Add devices <ul style="list-style-type: none"> <li>- A96L312</li> <li>- A96T346</li> <li>- MC97F6108M</li> </ul>
V2.028.00	2017. 4.10.Mon	Change <ul style="list-style-type: none"> <li>- Expand dump display cell width</li> <li>- Expand register display cell width</li> <li>- Do not reset the target MCU when it is attached.</li> </ul> Add

		<ul style="list-style-type: none"> <li>- A94B114 configuration dialog box</li> </ul> Add devices <ul style="list-style-type: none"> <li>- MC97F2664A</li> </ul>
V2.027.03	2017. 2.10.Fri	Fix <ul style="list-style-type: none"> <li>- If KEIL uVision5 C-project used separated output folders, source file mismatching problem occurred.</li> </ul>
V2.027.02	2017. 2. 7.Tue	Fix <ul style="list-style-type: none"> <li>- A94A332B : flash downloader image file was not included</li> </ul>
V2.027.01	2017. 1.23.Mon	Change <ul style="list-style-type: none"> <li>- If newer version found, ask to user . (download setup program and execute it).</li> </ul> KEIL DLL <ul style="list-style-type: none"> <li>- Add new DLL for code banking (ABOV_OCD_banked.DLL)</li> <li>- Distinguish banked and no banked DLL</li> <li>- Display checksum : no banked DLL only</li> </ul>
V2.026.00	2016.12.22.Thu	Change <ul style="list-style-type: none"> <li>- Environment file save "Keep code area address" within Download hex dialog box</li> </ul> Fix <ul style="list-style-type: none"> <li>- KEIL DLL : Allow XDATA edit</li> </ul>
V2.025.00	2016.12.15.Thu	Add devices <ul style="list-style-type: none"> <li>- MC94A332B</li> <li>- A96T228 distinguish from A96T218</li> <li>- A96R136</li> </ul> Add <ul style="list-style-type: none"> <li>- A97C450 option GUI</li> </ul> Modification <ul style="list-style-type: none"> <li>- MC96F7864 trim recover</li> </ul>
V2.024.00	2016.11. 1.Tue	Change <ul style="list-style-type: none"> <li>- 96F6432, 96F7464 option page erase -&gt; byte erase</li> <li>- 96FR364C boot area size</li> <li>- A96T316, A96T218</li> </ul>

		<p>Disable GPIO /reset input during Flash programming</p> <ul style="list-style-type: none"> <li>- Distinguish A96T316/326/336</li> </ul>
V2.023.01	2016. 8. 4.Thu	<p>Fix code area range</p> <ul style="list-style-type: none"> <li>- A96T218 : 24KB -&gt; 32KB</li> </ul>
V2.023.00	2016. 7.29.Fri	<p>Change option</p> <ul style="list-style-type: none"> <li>- A96T218 boot code size</li> </ul>
V2.022.01	2016. 4.12.Tue	<p>Fix devices</p> <ul style="list-style-type: none"> <li>- A96FT218, A96T316 : XDATA edit allowed</li> </ul> <p>Extend dump window address area width.</p>
V2.022.00	2016. 3.21.Mon	<p>Add devices</p> <ul style="list-style-type: none"> <li>- 96FR364C</li> <li>- 96F8204</li> <li>- 97C624</li> <li>- A96T218</li> <li>- A96T316</li> </ul> <p>Modify 96FT242 option display (/Reset)</p>
V2.021.00	2015.10.14.수	<p>Change IRC backup from trim, not user option</p> <ul style="list-style-type: none"> <li>- 97F6108A</li> <li>- 97F8324H</li> </ul>
V2.020.00	2015. 9. 7.Mon	<p>Support newer S/W version check via internet</p> <p>Add 97F68128A</p> <p>Improve CFG backup</p> <ul style="list-style-type: none"> <li>- 96F6832/7064/7816</li> </ul>
V2.019.00	2015. 7.27. Mon	<p>Update code, XDATA every 1 sec during OCD2 device emulation</p>
V2.018.00	2015. 7.23.Thu	<p>Add devices</p> <ul style="list-style-type: none"> <li>- 93F5632</li> <li>- 96F1206</li> </ul> <p>Modify 97F2664 page erase to byte erase</p> <p>Support global variable save/load function</p> <p>Fix large/small icon display bug</p> <p>Update IDATA every 1 sec during OCD2 device emulation</p>
V2.017.00	2015. 5. 8.Fri	<p>Add devices</p> <ul style="list-style-type: none"> <li>- 97FG316</li> </ul>

		<ul style="list-style-type: none"> <li>- 96F7848C : avoid bulk erase bug</li> </ul> Warning notice : device with bit branch bug
V2.016.00	2015. 3.24.Tue	Add devices <ul style="list-style-type: none"> <li>- 96FT1704, 96FK102</li> <li>- MPW : 94FB704, 97F1202A</li> </ul> Fix symbol download error for MS-Windows8 Support OCD2 isolator dongle
V2.015.01	2014.12.17Wed	Fix device option SPEC. error <ul style="list-style-type: none"> <li>- Move 96F6809 VAPEN option bit</li> </ul>
V2.015.00	2014.12.10. Wed	Support English, Chinese display switching Support 97F60128 double buffering Change device option SPEC. <ul style="list-style-type: none"> <li>- Change 97F6108A default I/O option</li> <li>- Change 96FD316 option</li> <li>- Move 97F60128 /reset, boot size</li> </ul> Add devices <ul style="list-style-type: none"> <li>- 96FM408, 96F8104, 96F6809</li> </ul>
V2.014.00	2014. 8.12. Tue	Support Windows XP (32bit)
V2.013.00	2014. 7.29. Mon	Add devices <ul style="list-style-type: none"> <li>- 93F5616</li> <li>- 96FC364, 96FR112C</li> <li>- 97F8264 : expand option to 4 page</li> </ul>
V2.012.00	2014. 3.18. Tue	Change option SPEC. <ul style="list-style-type: none"> <li>- Invert 96FT242 /Reset pin select option</li> </ul>
V2.011.00	2013. 9.17. Tue	Add OCD2 I/F voltage selecting option Enable step over function Support CM8051 core Support long length line source file Improve Hex download, select unused area to 0x00/0xFF Add devices <ul style="list-style-type: none"> <li>- 97F2208H EVA</li> <li>- 96F6416P, 96F116C</li> <li>- 96FD316, 96FT242</li> <li>- 97F6064, 97F6108A, 97F8264</li> </ul>
V2.010.00	2013. 8.28. Wed	Change option SPEC.

		<ul style="list-style-type: none"> <li>- Remove 97F1316 AMP option</li> </ul> Add devices <ul style="list-style-type: none"> <li>- 96FT1616, 96FT1504</li> <li>- 97F60128</li> <li>- 96FR332B</li> <li>- 97F1316 EVA rev.</li> </ul>
V2.009.00	2013. 5.27. Mon	<p>If Hex file size is bigger than device code, remove extra hex</p> <p>Improve OCD2 device OSC measuring</p> <p>Add checksum function</p> <p>Support extended SFR</p> <ul style="list-style-type: none"> <li>- XSP, XSPCR( if device embedded it)</li> </ul> <p>Add devices</p> <ul style="list-style-type: none"> <li>- 93F5516</li> <li>- 96FR116B, 96FR364B</li> <li>- 96F6508A, 96FB504</li> <li>- 97F38128 test device</li> <li>- 97F1316 EVA</li> <li>- 97F2208H EVA MPW</li> <li>- MCE8112</li> </ul>
V2.008.00	2012. 9.18. Tue	<p>Fix dump window</p> <ul style="list-style-type: none"> <li>- Scroller offset</li> </ul>
V2.007.00	2012. 8. 6. Mon	<p>Disable step over function</p> <ul style="list-style-type: none"> <li>- It is still unstable</li> </ul> <p>Omit duplicate source line number</p>
V2.006.00	2012. 7.17. Tue	<p>Environment file name use Mac address</p> <p>Check Server based OS environment</p>
V2.005.00	2012. 7.12. Thu	<p>Fix Cypress F/W downloader</p>
V2.004.00	2012. 7.10. Tue	<p>Improve readability of OCD dongle initialize dialog box</p> <p>Support symbol endian</p> <ul style="list-style-type: none"> <li>- Big endian : OMF, OMF2</li> <li>- Little endian : CDB</li> </ul> <p>Improve disassemble window</p> <ul style="list-style-type: none"> <li>- Support mouse wheel scroll</li> <li>- Add vertical scroll bar</li> </ul>
V2.003.00	2012. 6.14. Thu	<p>Fix RAM break dialog popup when using OCD1 dongle</p>

V2.002.00	2012. 6. 8. Fri	Improve I/F with low freq. target
V2.001.00	2012. 6. 7. Thu	Improve time delay function Add ADC2011
V2.000.00	2012. 5.25. Fri	Support OCD1 and OCD2 dongle First released version





	MC96P0202EVA A96G140 A96G174 A96L302 A96L312 A96L322 A96R136 A96R140 A96R150 A96R725 A96R739 A96T218 A96T316 A96T346 A96T356	MC96P1102EVA          A96T228 A96T316-ADC	MC96P6608EVA          A96T326	          A96T336
MC/A97	MC97F1206EVA MC97F2664 MC97F38128 MC97F60128 MC97F6108A MC97F6108M MC97F68128A MC97F8264 MC97FG316 A97C450 A97C624	MC97F1316EVA   MC97F3864 MC97F6064   MC97F8324H	MC97F2208EVA	
Etc	MC91CS801	MCE811	MH2E23N	AL4000