

Factory Programming Order & VERIFICATION SHEET

MC96F6432 - **MYF**



Package Information : Q, D, M

Please fill in the bold areas.

1. Customer Information

Company Name	<input type="text"/>
Application	<input type="text"/>
Order Date	YYYY MM DD
Tel :	<input type="text"/>
Fax :	<input type="text"/>
Name & : Signature	<input type="text"/>

2. Device Information

Package	<input type="checkbox"/> 44 MQFP (Q) <input type="checkbox"/> 32 SOP (D) <input type="checkbox"/> 28 SOP (M) <input type="checkbox"/> Wafer Chip sales code:()
ROM Code Size	32 K byte

4. Writing Option

<input type="checkbox"/> Enable Code Read Protection <input type="checkbox"/> Enable Code Write Protection <input type="checkbox"/> Enable RESETB pin (P55) <input type="checkbox"/> Enable Specific Area Write Protection <input type="checkbox"/> 3.8KB (0x100~0xFFFF) <input type="checkbox"/> 1.7KB (0x100~0x7FF) <input type="checkbox"/> 768B (0x100~0x3FF) <input type="checkbox"/> 256B (0x100~0x1FF)
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(Please check mark info)

3. OTP file Information

Check Sum	<input type="text"/>
File Name	<input type="text"/> .HEX
<p>You must fill unused area with 0x00 (Code area : 0000_H ~ 7FFF_H)</p>	

5. ROM CODE Verification

Verification Date :	<input type="text"/>
Check Sum :	<input type="text"/>
Name:	<input type="text"/>

6. Remark

Marking : standard
